Worksheet 2 Part 1

Worksheet 2 is split into two parts, this is the first part, with a second part to follow. Each part is worth 50% of the total for worksheet 2, which itself is worth 50% of the module assessment, with worksheet 1 making up the other 50%.

# Starting a Tiny OS

Marking scheme

* Task 1 ‑ 20%
* Task 2 ‑ 20%
* Task 3 ‑ 40%
* Task 4 ‑ 20%

All code should be submitted via a <gitlab.uwe.ac.uk> repo.

Each task should be documented in a README.md in the main repo, including screen shots of the code running, and details of how your implementation works.

After completing this worksheet you should be familiar with:

* The basics of boot a machine from scratch
* Calling C from assembler
* Developing a framebuffer driver for our Tiny OS

It is assumed that you have completed worksheets 0 and 1 before starting this worksheet. The deadline for completing this worksheet is 5th December, 2024, by 2pm.

IMPORANT: All code must be submitted via gitlab, failure to provide a link to Gitlap repo will result in a mark of 0. No email or other form of submission is acceptable. There will be in class vivas week 17 and 18, i.e. weeks stating the 9th and 16th of December, 2024.

You need to have the book [The Little Book of OS Development](https://blackboard.uwe.ac.uk/bbcswebdav/pid-10804049-dt-content-rid-49508230_2/xid-49508230_2) on hand to help gain a full understand‑ ing of what needs to be done to complete the tasks.

# To proceed

Before continuting with the worksheet it is recommended that you create a new folder in the repo that you used for worksheet 1, and all work for worksheet 2 will be in that directory.

For the first part of worksheet 2 we are going to work through chapters 1 ‑ 4 of the [The Little Book](https://blackboard.uwe.ac.uk/bbcswebdav/pid-10804049-dt-content-rid-49508230_2/xid-49508230_2) [of OS Development](https://blackboard.uwe.ac.uk/bbcswebdav/pid-10804049-dt-content-rid-49508230_2/xid-49508230_2). In particular we are going to develop a Tiny Operating System that boots the machine from scratch, which will require us to boot the machine via a boot loader, something that happens automatically with an OS already installed, and in the process we will learn how to call C from assembler and write output the framebuffer.

You will need to read the chapters to be able to complete these tasks.

## Task 1

For our OS we will need to have a number of different files, some that will be used to build the boot loaded, others that will be the kernel, and some that will describe how to put it all together. For this it is recomended that you create the following directory structure in the root of the directory created above:

drivers source iso

boot

grub

.gitignore Makefile README.md

As you develop the OS other files will be added, but this is the minimum. As in chapter 2 of the book we will begin with developing the smallest operating system possible, the one thing this OS will do is write **0xCAFEBABE** to the eax register! The cool thing about this is that is actually is the basis of everything else we will do and boots the machine from scratch.

Our first kernel has to be written in assembler as unlike the code in worksheet 1 there is no C stack and no C library to make our live simpler, we need to provide it all, and in the next task we will develop code to call into C and start working in C.

global loader

**;** the entry symbol **for** ELF

MAGIC\_NUMBER equ 0x1BADB002

FLAGS equ 0x0

CHECKSUM equ **-**MAGIC\_NUMBER

**;** define the magic number constant

**;** multiboot flags

**;** calculate the checksum

**; (**magic number **+** checksum **+** flags should

↪ equal 0**)**

section **.**text**:**

align 4

dd MAGIC\_NUMBER

↪ code**,**

dd FLAGS

dd CHECKSUM

**;** start of the text **(**code**)** section

**;** the code must be 4 byte aligned

**;** write the magic number to the machine

**;** the flags**,**

**; and** the checksum

loader**:**

↪ point in linker script**)** mov eax**,** 0xCAFEBABE

↪ register eax

**;** the loader label **(**defined as entry

**;** place the number 0xCAFEBABE in the

**.**loop**:**

jmp **.**loop

**;** loop forever

This code can also be found in our book and the only thing it doe of interest, is write the number

**0xCAFEBABE** in the register eax.

Put this in an assembler file loader.asm and as in worksheet 2 it can be compiled with the ‘nasm:

nasm -f elf loader.asm

Now in worksheet 1 we linked our assember files with a some C code that had a main function, how‑ ever, we as noted above we no longer have the ability to do that as there is no C library or stack. What are we to do?

As discused in the book, the code must be linked to produce an executable file, which requires some extra thought compared to when linking most programs, where we just used gcc or clang to link in our driver.c.

Here we are going to have to use a bootloader, in our case GRUB, to load the kernel at a memory address larger than or equal to 0x00100000 (1 megabyte (MB)), because addresses lower than 1 MB are used by GRUB itself, BIOS and memory‑mapped I/O. To acheive this we can use a custom linker script and the linker ld.

GRUB will transfer control to the operating system by jumping to a position in memory. Before the jump, GRUB will look for a magic number to ensure that it is actually jumping to an OS and not some random code. This magic number is part of the multiboot specification which GRUB adheres to, and explains where we had those magic numbers in our assemlber file above. Once

GRUB has made the jump, the OS has full control of the computer.

Therefore, the following linker script is needed (written for GNU LD) and should be added to link.ld

in our source directory:

ENTRY**(**loader**)**

*/\* the name of the entry label \*/*

SECTIONS **{**

**. =** 0x00100000**;**

*/\* the code should be loaded at 1 MB \*/*

**.**text ALIGN **(**0x1000**) :**

**{**

**\*(.**text**)**

**}**

*/\* align at 4 KB \*/*

*/\* all text sections from all files \*/*

**.**rodata ALIGN **(**0x1000**) :** */\* align at 4 KB \*/*

**{**

**\*(.**rodata**\*)** */\* all read-only data sections from all files \*/*

**}**

**.**data ALIGN **(**0x1000**) :**

**{**

**\*(.**data**)**

**}**

*/\* align at 4 KB \*/*

*/\* all data sections from all files \*/*

**.**bss ALIGN **(**0x1000**) :**

**{**

**\*(**COMMON**)**

**\*(.**bss**)**

**}**

*/\* align at 4 KB \*/*

*/\* all COMMON sections from all files \*/*

*/\* all bss sections from all files \*/*

**}**

We are now ready to link our executable, which can be done with the following command:

ld -T ./source/link.ld -melf\_i386 loader.o -o kernel.elf

Ok our kernel is now built. To make life a little simpler we are going to use GRUB Legacy, rather than a more complicated boot loader, but this means we need to copy a file into place that GRUB needs to work correclty. This file can be found in:

/opt/os/stage2\_eltorito

and needs to be copied into the directory:

iso/boot/grub

A configuration file **menu.lst** for GRUB must be created. This file tells GRUB where the kernel is located and configures some options, it is defined as follows:

**default=**0

timeout**=**0

title os

kernel **/**boot**/**kernel**.**elf

So if everything has gone correctly we should now have a directory structure as follows:

iso

**|**-- boot

**|**-- grub

**| |**-- menu.lst

**| |**-- stage2\_eltorito

**|**-- kernel.elf

Check this is correct and if so the following command can be used to create the ISO image:

genisoimage -R

-b boot/grub/stage2\_eltorito

-no-emul-boot

-boot-load-size 4

-A os

-input-charset utf8

-quiet

-boot-info-table

-o os.iso iso

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Ok now the kernel (os.iso) is built and we are ready to run it.

Unlike the book we will be use [qemu](https://www.qemu.org/), which is a generic and open source machine emulator and vir‑ tualizer. Using an emulator will allow us to not worry about actually booting our machine and not being able to do other stuff, but also we can use it to create a trace the machines execution and thus confirm that eax is indeed written too. To do this we can use the following command:

qemu-system-i386 -nographic -boot d -cdrom os.iso -m 32 -d cpu - D logQ.txt

This command tells **qemu** to boot a 32‑bit (i368) x86 machine, using our IOS image, output the ma‑ chines execution to the file loqQ.txt. As the last this our assembler code did was loop over and over, we will need to terminate the run with the command Ctrl-C. Once done we can open the logQ.txt file and search for the string 0xCAFEBABE. If you see EAX being assigned, then you have correctly booted!

To complete this task implement a makefile that builds the kernel, copies it into the GRUB directory structure, and then creates the ISO image. Add another rule, **run**, to your makefile that loads and runs the ISO image.

## Task 2

For this task you should read chapter 3 of the OS book and extend your kernel so that it can call C functions. As well as implementing the function sum\_of\_three, outlined in the chapter, you should implement and test at least two other C functions, although what those functions do is up to you.

Exend your makefile to work with this new structure, in particular, add support to transfer control to C from your loader.asm, developed in task 1.

## Task 3

For this task we are going to focus on adding some simple I/O, following chapter 4 of our OS book. An OS, or any program, the lack of IO makes it pretty basic and the ability to interact with the world opens up access to dynamically changing a programs behavour. The simplest form of I/O is the framebuffer so we can display or output text to the console. In part 2 of the worksheet, we will explore the other basic, but key I/O device, the keyboard.

There are often two different ways to interact with the hardware, memory‑mapped I/O and I/O ports.

The simplest kind of I/O device uses memory‑mapped I/O, which means you can write to a specific memory address and the hardware will be updated with the new data. One example of this is the

framebuffer. For example, if you write the value **0x410F** to address **0x000B8000**, you will see the letter **A** in white color on a black background.

If the hardware uses I/O ports then the assembly code instructions out and in must be used to commu‑ nicate with the hardware. The instruction out takes two parameters: the address of the I/O port and the data to send. The instruction in takes a single parameter, the address of the I/O port, and returns data from the hardware. One can think of I/O ports as communicating with hardware the same way as you communicate with a server using sockets. The cursor (the blinking rectangle) of the framebuffer is one example of hardware controlled via I/O ports on a PC.

To begin with you should work through the framebuffer section of chapter 4, where you will implement the very basics of implementing a framebuffer driver. Your driver code should be put in the directory drivers and you should extend your makefile to compile your additional files, when necessary.

Note, that tue to the fact that we are using qemu, which they do not use in the book, we need to make sure that the framebuffer is correctly configured to work with your application. All that need to do is run qemu in Curses mode.

Curses is a terminal control library for Unix‑like systems, enabling the construction of text user interface (TUI) applications

We can do this with the following command, assuming that the os.iso image is built:

qemu-system-i386 -curses -monitor telnet::45454,server,nowait -serial

↪ mon:stdio -boot d -cdrom os.iso -m 32 -d cpu -D logQ.txt

Other than putting qemu into curses mode we also told qemu to run a telnet service (on port 45454). This is important becuase once we put qemu in curses mode it can not longer receive keyboard com‑ mands (such as Ctrl-C) and thus it is not possible to quit our kernel once it has started running. Now we have to connect to the qemu process using telnet from another terminal window, with the command:

telnet 45454

Telnet is like SSH, but not encrypted, and once connected to qemu we can send the command:

quit

to tell qemu to exit.

Don’t forget to update your makefile with this new run command.

Finally, to complete this task you should extend you framework driver to a full API, in particular, it should expose it to the user as a 2D API, with the abilty to move the cursor to a location, given x and y, and to print strings, numbers, and so on. For example, this might include the following opertation to set the cursor postion:

void fb\_move**(**unsigned short x**,** unsigned short y**);**

The actual functions of your API are for you to define, but as noted they should include the functions described above, and also support for setting text colour, and clearing the framebuffer.

Write a kernel to test your functionality.

## Task 4

Document your work in your README.md and submit to Gitlab. The readme should contain code snippets, demostrating understanding, screen shots, and so on. It should not contain complete files of code, as these will be in the repo itself.